

What Is Claimed Is:

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1. ~~A method of driving a liquid crystal display panel~~
using a 2-dot inversion system, the method comprising the
steps of:

sequentially pre-charging a plurality of pixel cells of
the liquid crystal display panel along a plurality of gate
lines; and

sequentially charging the pixel cells with a plurality
of data signals along the gate lines after pre-charging the
pixel cells.

2. The method as claimed in claim 1, wherein the pre-
charging step is carried out when the pixel cells on the
preceding gate line charges the data signal.

3. The method as claimed in claim 1, wherein the pre-
charging step is carried out at the time interval the same
as the data signal charging step.

4. ~~A method of driving a liquid crystal display panel~~

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using a 2-dot inversion system, the method comprising the steps of:

applying signals having a polarity inverted every two gate lines to a plurality of source lines on the liquid crystal display panel; and

applying a plurality of gate signals having a width of two horizontal synchronization intervals and overlapping each horizontal synchronization interval to each gate line on the liquid crystal display panel.

5. A method of driving a liquid crystal display panel using a 2-dot inversion system, the method comprising the steps of:

allowing a plurality of pixel cells arranged on the liquid crystal display panel to cross a plurality of source lines and gate lines each other to charge a voltage stored in the pixel cell on the preceding gate line and a data signal on the source line; and

allowing the plurality of pixel cells to charge the data signal on the source line.

6. An apparatus for driving a liquid crystal display

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panel employing a 2-dot inversion system, comprising:

a liquid crystal panel having a plurality of pixel cells arranged to cross a plurality of source lines and gate lines each other;

a gate driver for applying a gate signal to each gate line such that pixel cells on the gate lines of the liquid crystal display panel sequentially charge data signals to each source line along the gate lines; and

a dual gate start pulse generator charging the pixel cells prior to the charged data signal to the source line.

7. The apparatus as claimed in claim 6, wherein the double gate shift pulse generator pre-charges the pixel cells when the pixel cell on the preceding gate line charges the data signal.

8. The apparatus as claimed in claim 7, wherein the dual gate start pulse generator allows the pixel cells to carry out the pre-charging in a time interval equal to an interval for charging the data signal.

logical operation of each of the first gate signals and each of second gate signals to be applied to the preceding gate line, thereby generating the first gate signals.

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